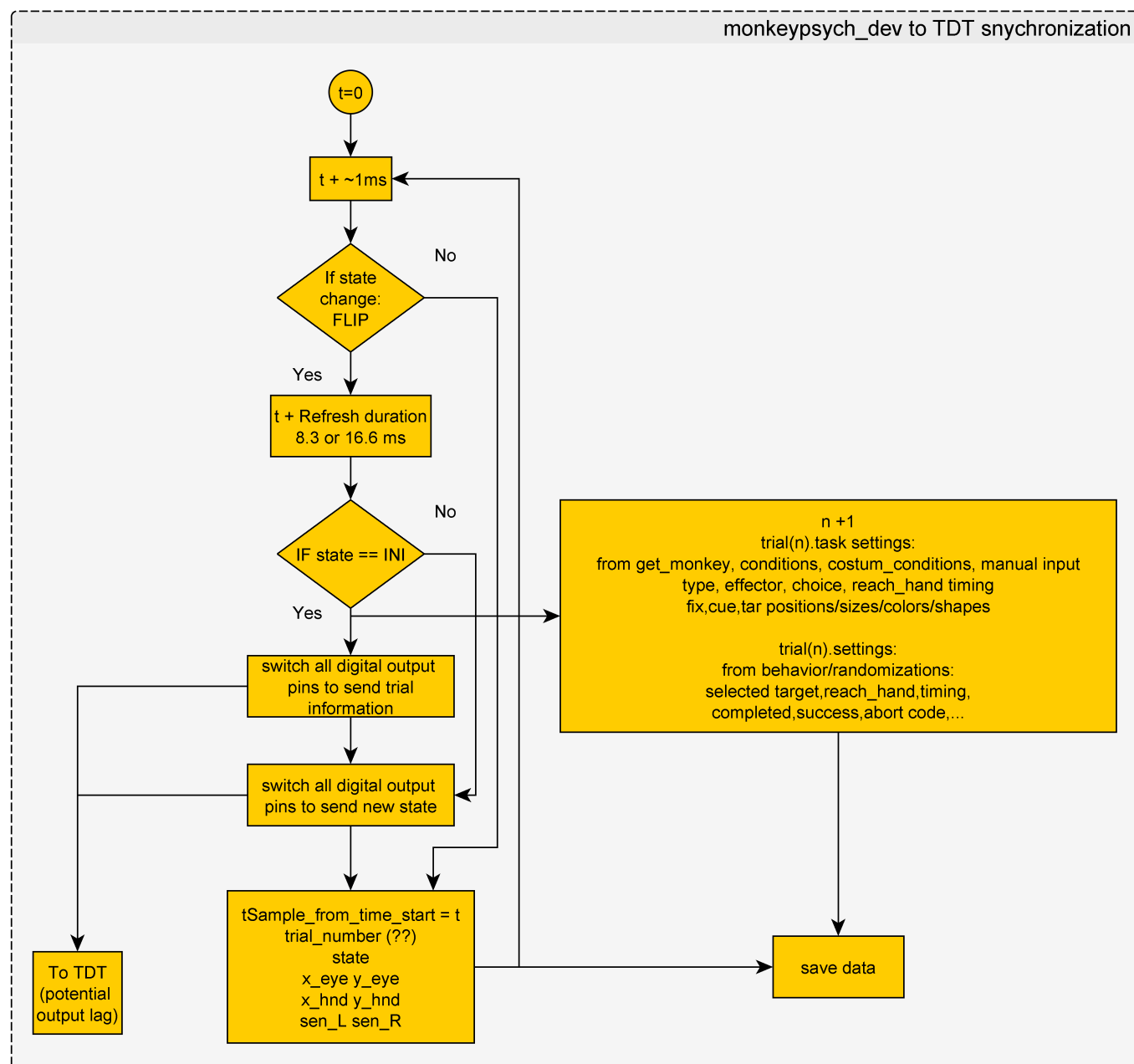


Synchronization

This section describes how synchronization is achieved

Synchronization during recording



Note that pins are only switched either to send trial information or after a state change (AND after the flip happened!) The idea is that both trial information and state information can be sent via the same channel. Details are described below.

Resulting 8 pin state signal

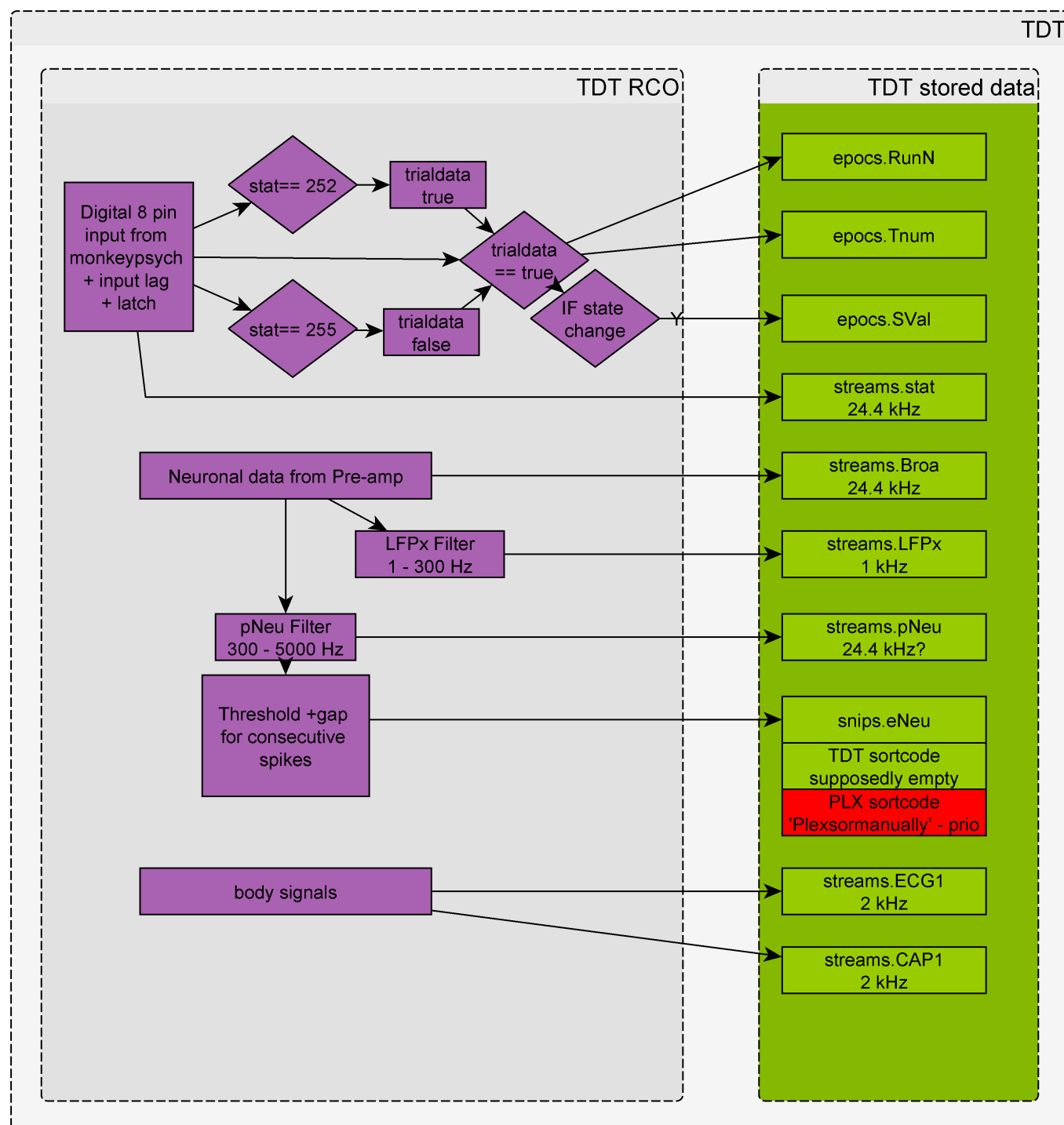


Values of 252 to 255 are preserved for indicating start of trial/state information and in-between trial information stoppers:

- 252: indicates start of trial information
- 254: in between trial information to define trialinfo packages
- 253: end of trial information
- 255: start of state information

Note that due to preserved values, a trial information package maximum value is limited. For example, to encode the date first package would be first 2 digits of the year, second package the last 2 digits of the year, third package the month and fourth package the day. That way none of the packages reaches a value of over 100. Trial information is sent during INI (state 1), during the other states the stat value reflects the current state.

Internal ROC deriving state onsets and trial information



Snippet and Stream synchronization in the combined trial structure

Last
update:
2022/12/29 07:15 ephys_pipeline:6_synchronization http://dag.dokuwiki.dpz.lokal/doku.php?id=ephys_pipeline:6_synchronization&rev=1641925144

From:
<http://dag.dokuwiki.dpz.lokal/> - **DAG wiki**

Permanent link:
http://dag.dokuwiki.dpz.lokal/doku.php?id=ephys_pipeline:6_synchronization&rev=1641925144

Last update: **2022/12/29 07:15**

